

Lattice CrossLink™ – Design Without Compromise

Programmable ASSP (pASSP™) Interface Bridges

Lattice Semiconductor developed the first programmable Application Specific Standard Product (pASSP™), CrossLink™, a low cost video interface bridge with the highest bandwidth, lowest power and smallest footprint. CrossLink's interface bridge supports leading protocols for mobile image sensors and displays ranging from virtual reality (VR) headsets, drones and DSLR cameras to smartphones, tablets and wearable devices. This past September, Lattice introduced a new use case for the CrossLink device as an optimized connectivity solution for Advanced Driver Assistance Systems (ADAS) and infotainment applications, while also bridging the gap between emerging image sensor and video display interfaces with legacy automotive interfaces. The CrossLink solution ushers in a new era of devices with its flexibility and fast time-to-market of an FPGA combined with the power and functional optimization of an ASSP, thus creating a new product class that delivers the best of both worlds. Embedded video designers need high performance, low power and compact interface bridges that can resolve image connectivity problems in a way that maximizes design flexibility and continues to encourage innovation. Lattice's CrossLink solution meets these needs through its comprehensive feature set, supporting a wide range of interfaces and protocols including MIPI D-PHY, MIPI CSI-2, MIPI DSI, as well as legacy video interfaces and protocols such as CMOS, RGB, MIPI DPI, MIPI DBI, SubLVDS, SLVS, LVDS and OpenLDI.